Docket No.: IRDM.129A

APR 1 5 2005 &

INFORMATION DISCLOSURE STATEMENT

icant : Sampsell et al.

App. No

10/731,989

Filed

December 9, 2003

For

AREA ARRAY MODULATION AND

LEAD REDUCTION IN

INTERFEROMETRIC MODULATORS

Examiner

Choi, William C.

Art Unit

2873

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

April 12, 2005

Mark M. Abumeri, Reg. No. 43,458

Dear Sir:

Enclosed for filing in the above-identified application is an Information Disclosure Statement by Applicant (PTO/SB/08 equivalent) listing 66 references to be considered by the Examiner, which includes 21 foreign patent references and non-patent literature as listed on the Information Disclosure Statement. Hardcopies of the 21 foreign and non-patent literature references are enclosed herewith.

This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

By:

04/15/2005 FMETEKII 00000029 10731989

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Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated:

Mark M. Abumeri Registration No. 43,458

Attorney of Record

Customer No. 20,995

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	Application No.	10/731,989
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CTETEMENT BY ADDITIONIT	First Named Inventor	Sampsell, Jeffrey Brian
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SHEET 1 OF 3	Attorney Docket No.	IRDM.129A

			U.S. PATENT	DOCUMENTS	
Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
	1	2,534,846	12-19-1950	Ambrose et al.	
	2	3,439,973	04-22-1969	Paul et al.	
	3	3,443,854	05-13-1969	Weiss	
	4	3,653,741	04-04-1972	Marks	
	5	3,656,836	04-18-1972	de Cremoux et al.	
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	8	4,099,854	07-11-1978	Decker et al.	
	9	4,228,437	10-14-1980	Shelton	
	10	4,377,324	03-22-1983	Durand et al.	
-	11	4,389,096	06-21-1983	Hori et al.	
	12	4,403,248	09-06-1983	te Velde	
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	29	5,231,532	07-27-1993	Magel et al.	
	30	5,233,459	08-03-1993	Bozler et al.	

Examiner Signature

Date Considered

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

T¹ - Place a check mark in this area when an English language Translation is attached.

10/731,989 Application No. INFORMATION DISCLOSURE December 9, 2003 Filing Date First Named Inventor Sampsell, Jeffrey Brian STATEMENT BY APPLICANT 2873 Art Unit (Multiple sheets used when necessary) Examiner Choi, William C. IRDM.129A SHEET 2 OF 3 Attorney Docket No.

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	31	5,311,360	05-10-1994	Bloom et al.	
	32	5,381,253	01-10-1995	Sharp et al.	
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	45	5,825,528	10-20-1998	Goosen	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials Cite No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-iss number(s), publisher, city and/or country where published.		T ¹	
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	49	CONNER, "Hybrid Color Display Using Optical Interference Filter Array," SID Digest, pp. 577-580 (1993).	
	50	GOOSSEN et al., "Possible Display Applications of the Silicon Mechanical Anti-Reflection Switch," Society for Information Display (1994).	
	51	GOOSSEN et al., "Silicon Modulator Based on Mechanically-Active Anti-Reflection Layer with 1Mbit/sec Capability for Fiber-in-the-Loop Applications," IEEE Photonics Technology Letters (Sep. 1994).	

Examiner Signature	Date Considered

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	HOWARD et al., "Nanometer-Scale Fabrication Techniques," VLSI Electronics: Microstructure Science, vol. 5, pp. 145-153 and pp. 166-173 (1982). JACKSON, "Classical Electrodynamics," John Wiley & Sons Inc., pp. 568-573 (date unknown).			ics: Microstructure	
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